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**Assignment 8 Report**

**Title \_\_Pipelined MIPS Processor & I/O Interface\_\_**

**Semester \_\_\_\_\_\_\_\_\_\_\_ Date \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_**

**by**

**Name \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ SID \_\_\_\_\_\_\_\_\_\_\_\_\_**

**Name \_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_\_ SID \_\_\_\_\_\_\_\_\_\_\_\_\_**

**Lab Checkup Record**

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **Week** | **Performed by (signature)** | **Checked by (signature)** | **Tasks**  **Successfully Completed** | **Tasks Partially Completed\*** | **Tasks Failed or Not Performed\*** |
| 1 |  |  |  |  |  |
| 2 |  |  |  |  |  |
| 3 |  |  |  |  |  |
| 4 |  |  |  |  |  |

**\* Explanation and/or analysis must be given in the report.**

**San Jose State University**

**Department of Computer Engineering**

**CMPE 140 Lab Report**